

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S4	1015	bin with processor	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 16:18
S5	11413	bin\$5 with processor	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 16:18
S6	73	binning with processor	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 16:29
S7	9	(defect\$4 with processor) and (performance with bin\$4)	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 16:33
S8	55	( partial\$4 with defect\$4 with processor)	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 16:57
S9	202	714/3.ccls.	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 17:11
S10	516	700/108.ccls.	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 17:12
S11	1216	700/121.ccls.	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 18:13
S13	257	700/109.ccls.	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 18:27
S14	42	700/109.ccls. and bin\$4	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 18:30
S15	50	700/110.ccls. and bin\$4	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 18:34
S16	1695	716/4.ccls.	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 18:34
S19	67	binning and (floating adj2 point)	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 18:37
S20	20	binning and (floating adj2 point) and ("IC" or (integrated adj circuit))	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 18:37
S22	892	702/182.ccls.	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 18:52

S23	155	702/182.ccls. and bin\$4	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 18:56
S24	289	702/81.ccls.	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 18:56
S25	62	702/81.ccls. and bin\$4	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 18:59
S26	266	324/719.ccls.	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 18:59
S28	242	712/222.ccls.	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 19:00
S31	11	712/227.ccls. and bin\$4 and (not binary)	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 19:01
S32	196	714/36.ccls.	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 19:02
S34	877	714/47.ccls.	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 19:02
S37	38	(integrated adj circuit) with bin\$4 with performance	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 19:07
S38	221	(bin\$4 with performance) and (unit with register)	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 19:08
S39	81	(bin\$4 with performance) and (unit with register) and (floating adj point)	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 19:08
S40	26877	cull\$4	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 19:17
S41	9394	cull\$4 and bin\$4	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 19:17
S42	51	cull\$4 and binning	USPAT; US-PGPUB; DERWENT	OR	OFF	2005/03/31 19:48
S43	327	(integrated adj circuit) and (defective with unit) and bin\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 19:50
S44	6	(integrated adj circuit) and (defective with unit) and binning	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:34

S45	0	(integrated adj circuit) and (defective with funtional with unit) and bin\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 19:50
S46	14	(integrated adj circuit) and (defective with functional with unit) and bin\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 19:54
S47	254	beffa.in.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 19:54
S48	52	beffa.in. and bin\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 19:58
S49	2	"6075216".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 19:59
S50	2	"6055463".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:00
S51	2	"5865319".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:00
S52	2	"5642307".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:00
S53	2	"5603412".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:01
S54	2	"5581510".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:01
S55	2	"5568408".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:02
S56	1	"2002/0162080"	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:02
S57	0	"2002/0162080A1"	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:02
S58	7	takahashi.in. and (716/7.ccls.)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:03
S59	1	milor.in. and (716/19.ccls.)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:04
S60	1	orshansky.in. and (716/5.ccls.)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:04

S61	2	"6549880".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:04
S62	2	"6434704".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:05
S63	2	"6415423".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:05
S64	2	"6397170".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:05
S65	2	"6345379".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:05
S66	2	"6272666".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:06
S67	2	"5956566".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:06
S68	2	"5861660".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:07
S69	2	"5917197".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:07
S70	2	"5726458".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:07
S71	2711	(processor with circuit) same bin\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:08
S72	404	(processor with integrated with circuit) same bin\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:08
S73	208	(processor with integrated with circuit) same bin\$4 and performance	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:09
S74	208	((processor with integrated with circuit) same bin\$4) and performance	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:09
S75	122	((processor with integrated with circuit) with bin\$4)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:09
S76	9	((processor with integrated with circuit) with bin\$4) and (not binary)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:10

S77	0	((processor with integrated with circuit) with yeild)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:10
S78	38	((processor with integrated with circuit) with yield)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:12
S79	14	(defective with processor) and (functional with unit) and performance and bin\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:16
S80	1433	(processor with bin)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:16
S81	526	(processor with bin) and test\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:16
S82	1556	((integrated adj circuit) with test\$4) and processor and bin\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:17
S83	298	((integrated adj circuit) with test\$4) and processor and bin\$4 and (not binary)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:17
S84	77	((integrated adj circuit) with test\$4) same processor) and bin\$4 and (not binary)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:26
S85	2765	324/765.ccls.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:26
S86	1077407	324/765.ccls. bin\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:26
S87	291	324/765.ccls. and bin\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:27
S88	143	324/765.ccls. and bin\$4 and (not binary)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:34
S89	448	((integrated adj circuit) with (sort\$4 or bin\$4)).ti.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:35
S90	87	((integrated adj circuit) with (sorting)).ti.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 20:40
S91	4	((integrated adj circuit)) and (partially adj2 defective adj2 processor)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/03/31 21:05
S92	2	"6067633".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 10:03

S93	2	"4722084".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 10:03
S94	2	"5276893".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 10:03
S95	2	"6021511".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 10:32
S96	2	"6038682".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 10:33
S97	2	"6067633".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 10:33
S98	2	"5596759".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 10:56
S99	2	"5504670".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 11:23
S10 0	1695	716/4.ccls.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 11:23
S10 1	2	"5440724".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 12:56
S10 2	2	"5423024".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 12:56
S10 3	2	"5136498".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 12:57
S10 4	2	"5051994".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 12:57
S10 5	2	"5045995".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 12:58
S10 6	2	"5042008".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 12:58
S10 7	2	"4958273".pn.	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 12:58
S10 8	48	714/36.ccls. and (test\$4 with processor)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 13:14

S10 9	53	defect\$4 with floating with point	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 13:27
S11 0	598	(floating with point with test\$4)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 13:28
S11 1	4	((floating and prediction) with point with test\$4)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 13:31
S11 2	62	(integer adj2 unit) and (floating adj2 unit) and test\$4 and defect\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 14:50
S11 3	1	(integer adj2 unit with defect\$4) and (floating adj2 unit with defect\$4)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 14:51
S11 4	8	(integer adj3 unit with defect\$4)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 14:53
S11 5	8	(floating adj3 unit with defect\$4)	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 16:26
S11 6	33	processor with benchmark with test\$4	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 16:32
S11 7	1	processor with benchmark with look\$4 with table	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 16:33
S11 8	30	benchmark with look\$4 with table	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 16:36
S11 9	165	processor with floating with point with branch with integer	USPAT; US-PGPUB; DERWENT	OR	ON	2005/04/01 16:36